

Docket No.: 061282-0044



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
Shinichi ABE : Confirmation Number: 8479  
Application No.: 10/697,650 : Group Art Unit: 2112  
Filed: October 31, 2003 : Examiner: MISIURA, Brian Thomas  
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND INTERRUPT REQUEST OUTPUT METHOD THEREOF

Mail Stop Amendment  
Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.  
☐ Applicant is entitled to small entity status under 37 CFR 1.27  
☒ Also attached: Drawing Amendment: Figure 5-- Prior Art

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	4	20	0	\$50.00 =	\$0.00
Independent Claims	4	4	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILLY & EMERY LLP

Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 MEF:dmd  
Facsimile: 202.756.8087  
Date: June 21, 2006

Please recognize our Customer No. 20277 as our  
correspondence address.



Docket No.: 61282-044

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
<b>Shinichi Abe</b>	:	Confirmation Number: 8479
	:	
Serial No.: 10/697,650	:	Group Art Unit: 2112
	:	
Filed: October 31, 2003	:	Examiner: Brian Thomas MISIURA
	:	
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND INTERRUPT REQUEST OUTPUT METHOD THEREOF	:	

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Mail Stop Amendment  
Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 21, 2006, having a three-month shortened statutory period for response set to expire on June 21, 2006, please amend the above-identified application as follows: